Inventors: Tetsuya OTSUKI, Hirofumi KUROSAWA, Hiroshi MIKI

Atty. Ref.: 9319I-000715

FIG. 1A

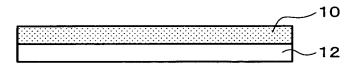


FIG. 1B

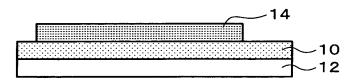


FIG. 1C

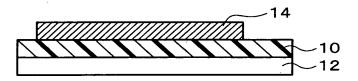
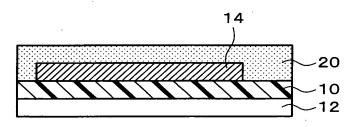


FIG. 1D



Title: WIRING BOARD, METHOD OF MANUFACTURING THE SAME, SEMICONDUCTOR DEVICE, AND ELECTRONIC INSTRUMENT

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FIG. 2A

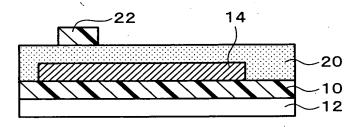


FIG. 2B

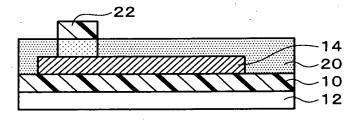


FIG. 2C

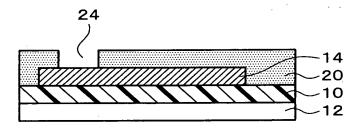
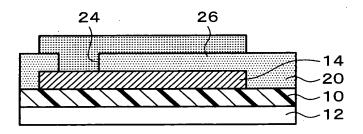


FIG. 2D



Title: WIRING BOARD, METHOD OF MANUFACTURING THE SAME, SEMICONDUCTOR DEVICE, AND ELECTRONIC INSTRUMENT

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FIG. 3A

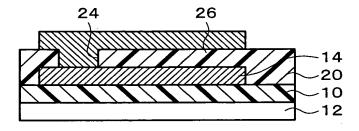


FIG. 3B

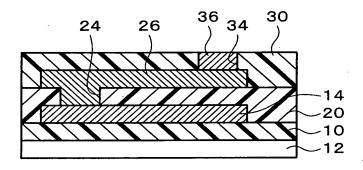
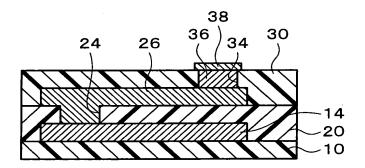


FIG. 3C



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FIG. 4A

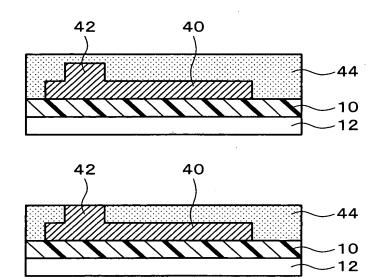
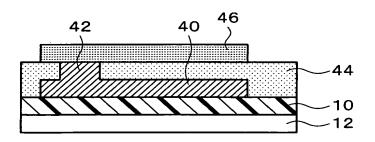


FIG. 4C

FIG. 4B



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FIG. 5A

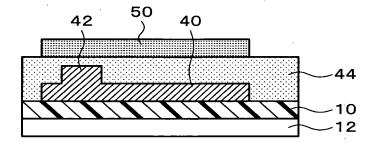
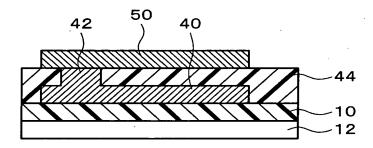


FIG. 5B



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FIG. 6A



FIG. 6B

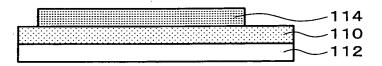
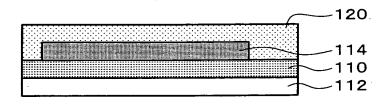


FIG. 6C



FIG. 6D



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FIG. 7A

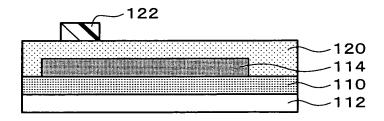


FIG. 7B

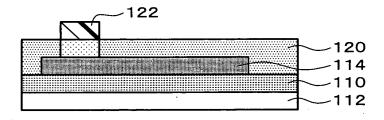


FIG. 7C

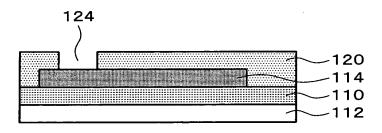
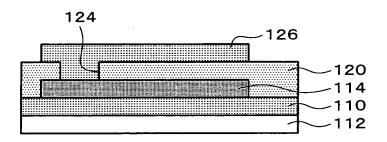


FIG. 7D



Title: WIRING BOARD, METHOD OF MANUFACTURING THE SAME, SEMICONDUCTOR DEVICE, AND ELECTRONIC INSTRUMENT

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FIG. 8A

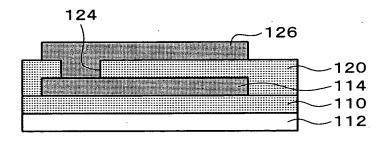


FIG. 8B

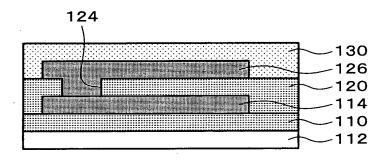
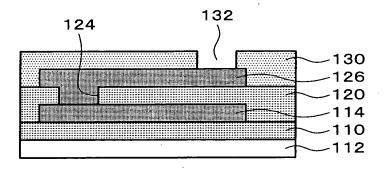


FIG. 8C



Title: WIRING BOARD, METHOD OF MANUFACTURING THE SAME, SEMICONDUCTOR DEVICE, AND ELECTRONIC INSTRUMENT

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FIG. 9A

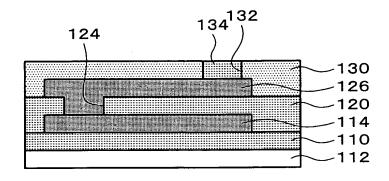


FIG. 9B

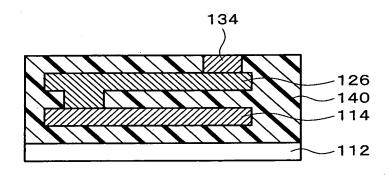
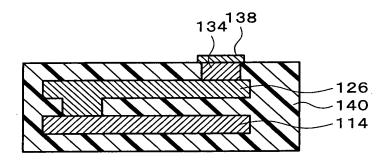


FIG. 9C



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FIG. 10A

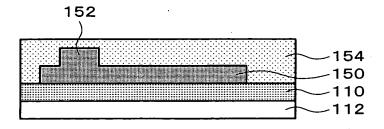
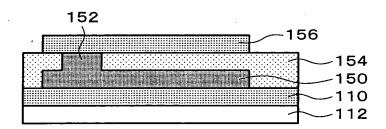


FIG. 10B



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FIG. 11A

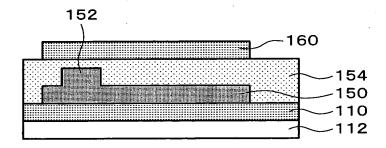
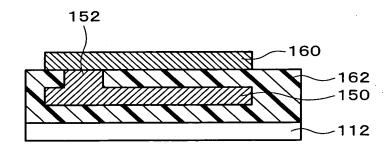


FIG. 11B



Title: WIRING BOARD, METHOD OF MANUFACTURING THE SAME, SEMICONDUCTOR DEVICE, AND ELECTRONIC INSTRUMENT

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FIG. 12

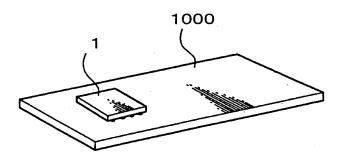


FIG. 13

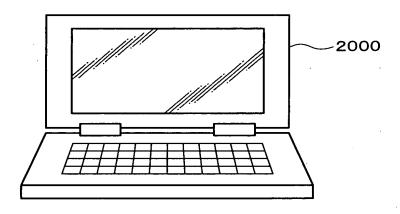


FIG. 14

